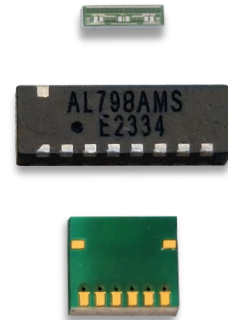


# AL798

## MagnetoResistive FixPitch Sensor (1 mm)

The AL798 is an Anisotropic MagnetoResistive (AMR) position sensor. The sensor contains two Wheatstone bridges shifted against each other. The output signals are proportional to sine and cosine of the coordinate to be measured (see Fig. 1).

The MR strips of this FixPitch sensor geometrically match to a pole length of 1 mm (equal to a magnetic period of 2 mm). Additionally, the sensor layout incorporates PerfectWave technology, i. e. the position of each block of MR strips has a special arrangement to filter higher harmonics and to increase the signal quality. The resistors in this FixPitch sensor are distributed over several poles (2), thus the errors in the magnetic measurement scale are reduced without any signal delay. The amplitude is almost constant in a wide working range between sensor and magnetic scale. The bond version of AL798 is available as bare die. For SMD processing, the sensor is available in a Sil6, LGA or SIL8 package.



### Product Overview of AL798

Article description	Package	Delivery Type
AL798ACA-AB <sup>1)</sup>	Die on Wafer	Waferbox
AL798ACA-AC	Bare Die	Waffle pack (192 pcs)
AL798AKA-AC	SIL6	Waffle pack (90 pcs)
AL798AMA-AE	LGA6S	Tape on reel (2000 pcs)
AL798AMB-AE	LGA6L (Double die)	Tape on reel (2000 pcs)
AL798AMS-AE	SIL8	Tape on reel (2000 pcs)
AL798AMS-AS	SIL8-D	Tape on reel (2000 pcs)

<sup>1)</sup> minimum order quantities apply.

### Quick Reference Guide

Symbol	Parameter	min.	typ.	max.	Unit
P	Pitch (magnetic pole length)	-	1.0	-	mm
V <sub>CC</sub>	Supply voltage	-	5.0	-	V
V <sub>off</sub>	Offset voltage per V <sub>CC</sub>	-2.0	-	+2.0	mV/V
V <sub>peak</sub>	Signal amplitude per V <sub>CC</sub>	9.0	11.5	14.0	mV/V
R <sub>B</sub>	Bridge resistance	2.4	3.6	4.8	kΩ

### Absolute Maximum Ratings

In accordance with the absolute maximum rating system (IEC60134).

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply voltage	-9.0	+9.0	V
T <sub>amb</sub>	Ambient temperature	-40	+125	°C
T <sub>stg(Die)</sub>	Storage temperature (Die)	-65	+150	°C
T <sub>stg(others)</sub>	Storage temperature (others)	-40	+125	°C

Stresses beyond those listed under "Absolute maximum ratings" may cause permanent damage to the device.

This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### Features

- Based on the Anisotropic MagnetoResistive (AMR) effect
- Contains two wheatstone bridges on Chip
- Sine and Cosine output
- Adapted to 1 mm poles
- PurePitch design (2 poles)
- PerfectWave technology
- Ambient temperature range from -40 °C to +125 °C

### Advantages

- Contactless angle and position measurement
- Large air gap
- Excellent accuracy
- Minimized offset voltage
- Negligible hysteresis

### Applications

Incremental or absolute encoder for linear or rotary movements in various industrial applications, such as:

- Motor integrated encoder
- Motorfeedback system
- Linear guide



## Magnetic Data

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
H <sub>ext</sub>	Magnetic field strength <sup>1)</sup>		15.0	25.0	-	kA/m

<sup>1)</sup> The stimulating magnetic field in the sensor plane to ensure minimum error specified in note 8.

## Electrical Data

T<sub>amb</sub> = +25°C, H<sub>ext</sub> = 25 kA/m; V<sub>CC</sub> = 5.0 V; unless otherwise specified.

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
V <sub>CC</sub>	Supply voltage		-	5.0	-	V
V <sub>off</sub>	Offset voltage per V <sub>CC</sub>	See Fig. 2	-2.0	-	+2.0	mV/V
TC <sub>Voff</sub>	Temperature coefficient of V <sub>off</sub> <sup>2)</sup>	T <sub>amb</sub> = (-40...+125)°C	-2.0	-	+2.0	(μV/V)/K
V <sub>peak</sub>	Signal amplitude per V <sub>CC</sub> <sup>3)</sup>	See Fig. 2	9.0	11.5	14.0	mV/V
TC <sub>Vpeak</sub>	Temperature coefficient of V <sub>peak</sub> <sup>4)</sup>	T <sub>amb</sub> = (-40...+125)°C	-0.48	-0.42	-0.36	%/K
R <sub>B</sub>	Bridge resistance <sup>5)</sup>		2.4	3.6	4.8	kΩ
R <sub>S</sub>	Sensor resistance <sup>6)</sup>		1.2	1.8	2.4	kΩ
TC <sub>RB</sub>	Temperature coefficient of R <sub>B</sub> <sup>7)</sup>	T <sub>amb</sub> = (-40...+125)°C	0.24	0.28	0.32	%/K
FIT	FIT-Rate		-	0.9	-	x10 <sup>9</sup> h
MTTF	Mean time to failure	At 55 °C	-	126839	-	years

<sup>2)</sup>  $TC_{Voff} = 100 \cdot \frac{V_{off}(T_2) - V_{off}(T_1)}{T_2 - T_1}$  with T<sub>1</sub> = +25°C; T<sub>2</sub> = +125°C.

<sup>3)</sup> Maximal output voltage without offset influences. Periodicity of V<sub>peak</sub> is sin(P) and cos(P).

<sup>4)</sup>  $TC_{Vpeak} = 100 \cdot \frac{V_{peak}(T_2) - V_{peak}(T_1)}{V_{peak}(T_{amb}) \cdot (T_2 - T_1)}$  with T<sub>1</sub> = +25°C; T<sub>2</sub> = +125°C.

<sup>5)</sup> Bridge resistance between +V<sub>O1</sub> and -V<sub>O1</sub>, +V<sub>O2</sub> and -V<sub>O2</sub>.

<sup>6)</sup> Sensor resistance between V<sub>CC</sub> and GND.

<sup>7)</sup>  $TC_{RB} = 100 \cdot \frac{R_B(T_2) - R_B(T_1)}{R_B(T_{amb}) \cdot (T_2 - T_1)}$  with T<sub>1</sub> = +25°C; T<sub>2</sub> = +125°C.

## Accuracy

T<sub>amb</sub> = +25°C, H<sub>ext</sub> = 25 kA/m; V<sub>CC</sub> = 5.0 V; unless otherwise specified.

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
ΔX	Measurement error <sup>8)</sup>		-	7.0	9.0	μm
k	Amplitude synchronism <sup>9)</sup>		-	0.1	1.0	% of V <sub>peak</sub>

<sup>8)</sup> ΔX = |X<sub>real</sub> - X<sub>measured</sub>| without offset influences due deviations from ideal sinusoidal characteristics (ascertained at an ideal magnetic scale).

<sup>9)</sup>  $k = 100 - 100 \cdot \frac{V_{Peak1}}{V_{Peak2}}$

## Dynamic Data

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
f	Frequency range		1.0 <sup>10)</sup>	-	-	MHz

<sup>10)</sup> No significant amplitude loss in this frequency range.

## General Data

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
P	Pitch (magnetic pole length)	See Fig. 1	-	1.0	-	mm
d	Distance <sup>11)</sup>		-	0.5	-	mm
T <sub>amb</sub>	Ambient temperature		-40	-	+125	°C

<sup>11)</sup> See Fig. 3 for detailed information.

Output Signal Information

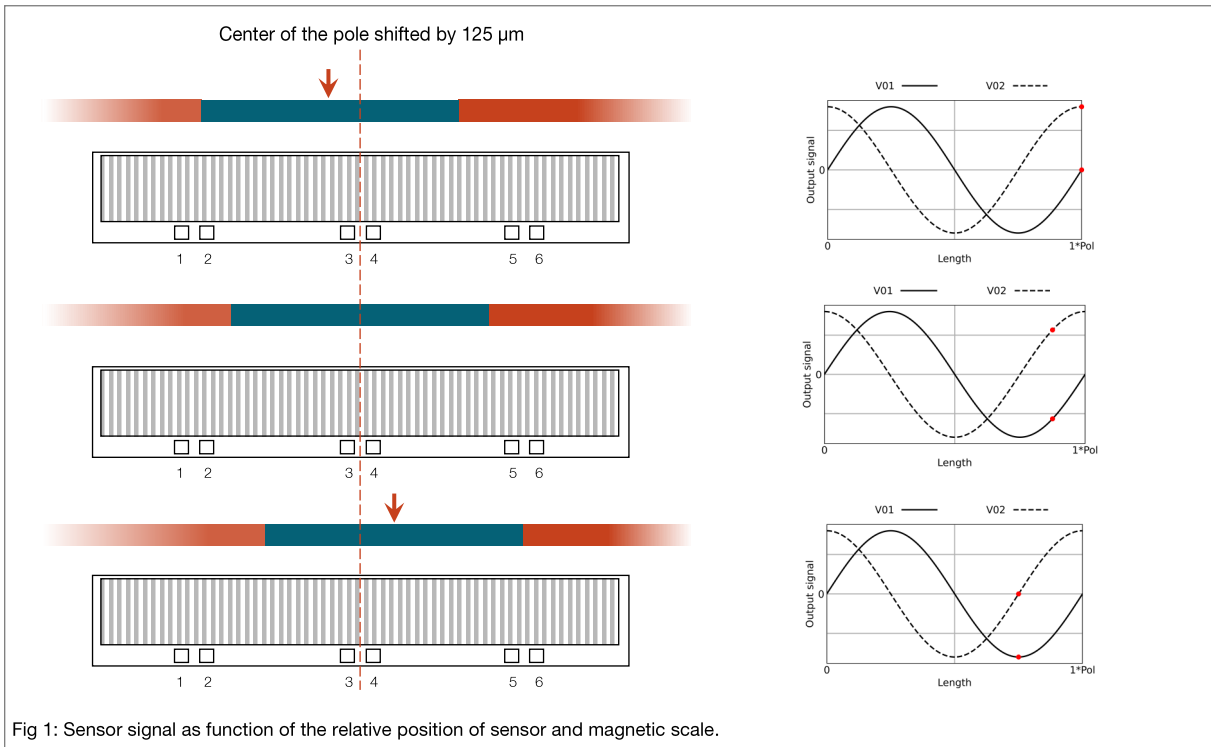


Fig 1: Sensor signal as function of the relative position of sensor and magnetic scale.

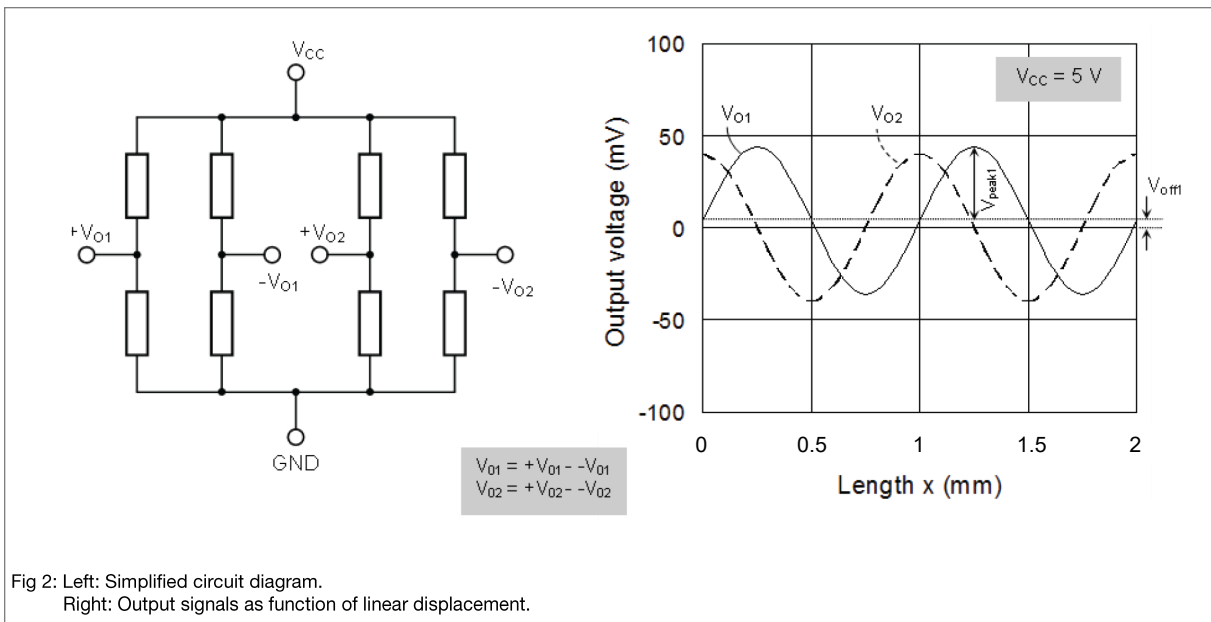


Fig 2: Left: Simplified circuit diagram.  
Right: Output signals as function of linear displacement.

Typical Performance Graphs

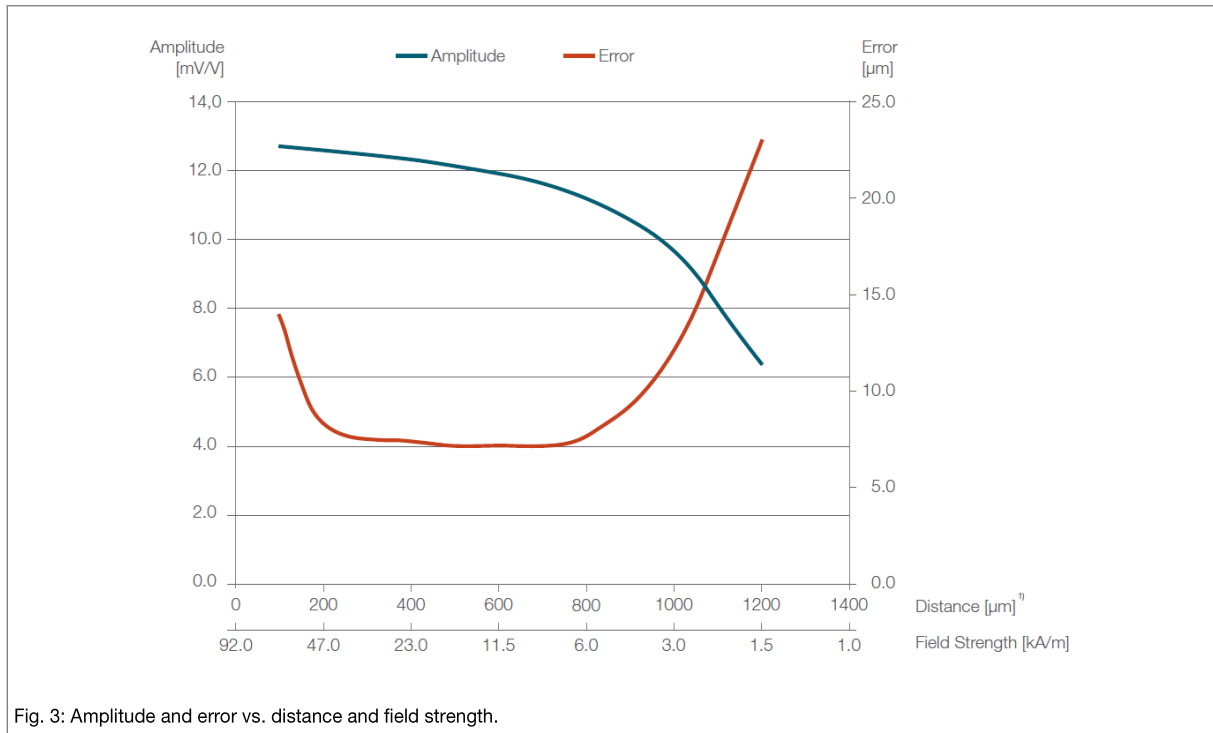


Fig. 3: Amplitude and error vs. distance and field strength.

<sup>1)</sup> In use with a plastic bounded hard ferrite magnetic scale (Br = 220 mT, thickness 1 mm, mounted on stainless steel),

### AL798ACA Bare Die

#### Pinout

Pad	Symbol	Parameter
1	+V <sub>O1</sub>	Positive output voltage bridge 1
2	+V <sub>O2</sub>	Positive output voltage bridge 2
3	GND	Ground
4	V <sub>CC</sub>	Supply voltage
5	-V <sub>O1</sub>	Negative output voltage bridge 1
6	-V <sub>O2</sub>	Negative output voltage bridge 2



Fig. 4: Pinout of the AL798ACA Bare Die

#### Dimensions

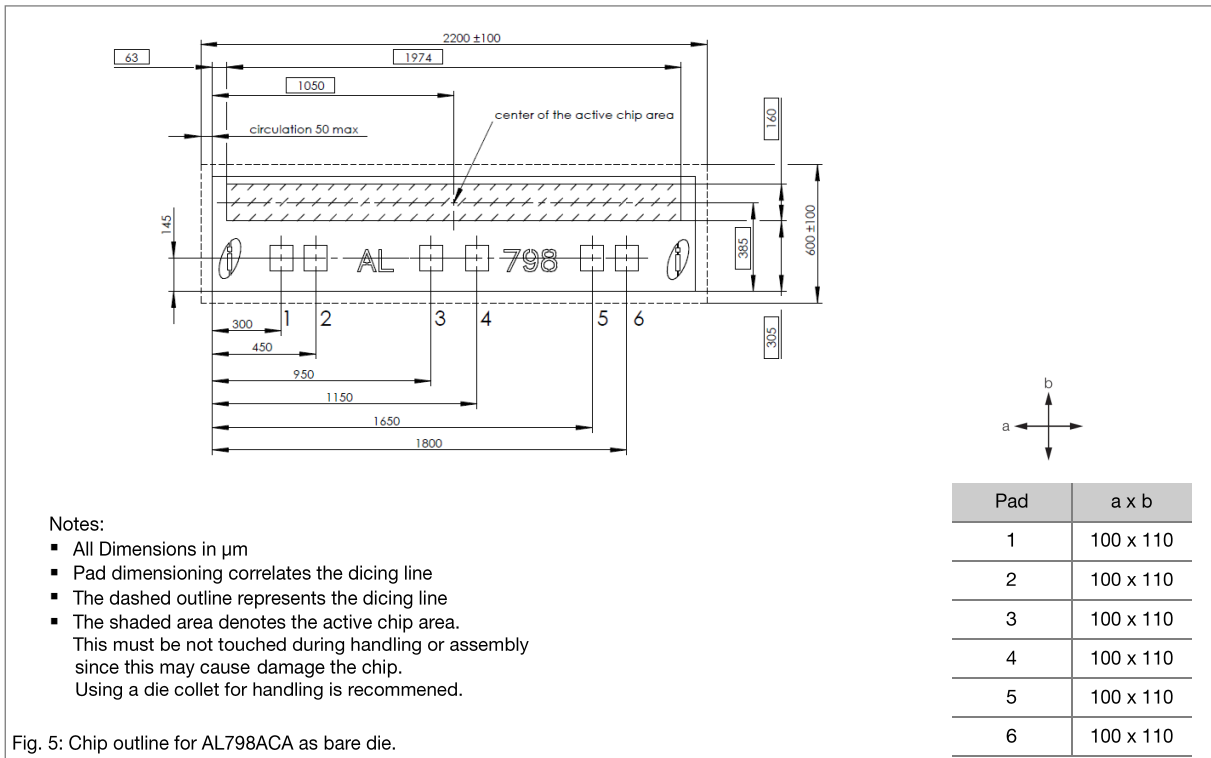


Fig. 5: Chip outline for AL798ACA as bare die.

#### Data for Packaging and Interconnection Technologies

Parameter	Value	Unit
Chip area <sup>1)</sup>	2.2 x 0.6	mm <sup>2</sup>
Chip thickness	525 ± 10	$\mu\text{m}$
Pad size	See Fig. 5	-
Pad thickness	0.8	$\mu\text{m}$
Pad material	AICu	-

<sup>1)</sup> Tolerances of chip see Fig. 5.

### AL798AKA SIL6 Package

#### Pinout

Pad	Symbol	Parameter
1	+V <sub>O1</sub>	Positive output voltage bridge 1
2	+V <sub>O2</sub>	Positive output voltage bridge 2
3	GND	Ground
4	V <sub>CC</sub>	Supply voltage
5	-V <sub>O1</sub>	Negative output voltage bridge 1
6	-V <sub>O2</sub>	Negative output voltage bridge 2

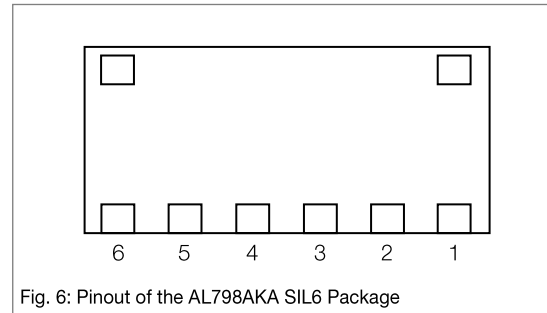


Fig. 6: Pinout of the AL798AKA SIL6 Package

#### Dimensions

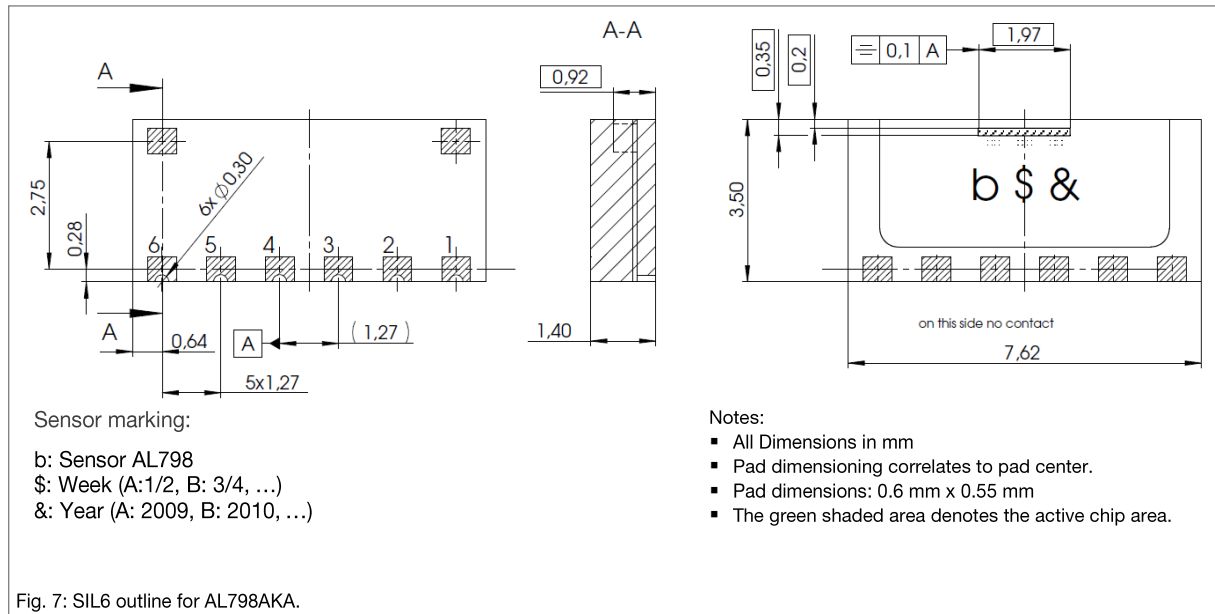
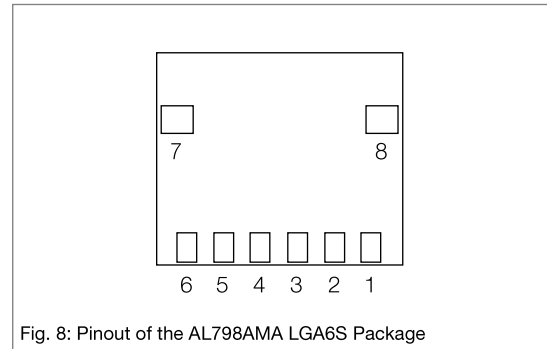


Fig. 7: SIL6 outline for AL798AKA.

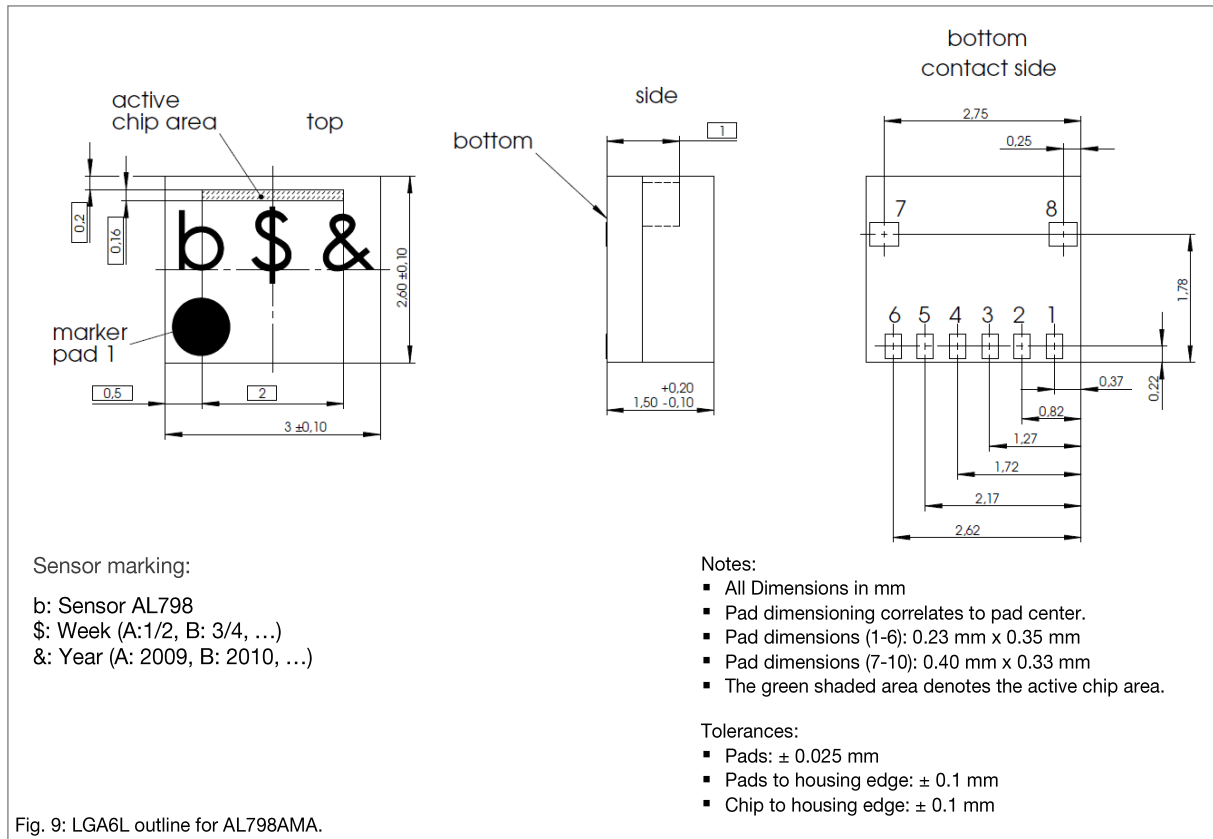
AL798AMA LGA6S Package

Pinout

Pad	Symbol	Parameter
1	+V <sub>O1</sub>	Positive output voltage bridge 1
2	+V <sub>O2</sub>	Positive output voltage bridge 2
3	GND	Ground
4	V <sub>CC</sub>	Supply voltage
5	-V <sub>O1</sub>	Negative output voltage bridge 1
6	-V <sub>O2</sub>	Negative output voltage bridge 2
7-10	NC	Not connected



Dimensions



### AL798AMA LGA6S Package

#### Reel layout

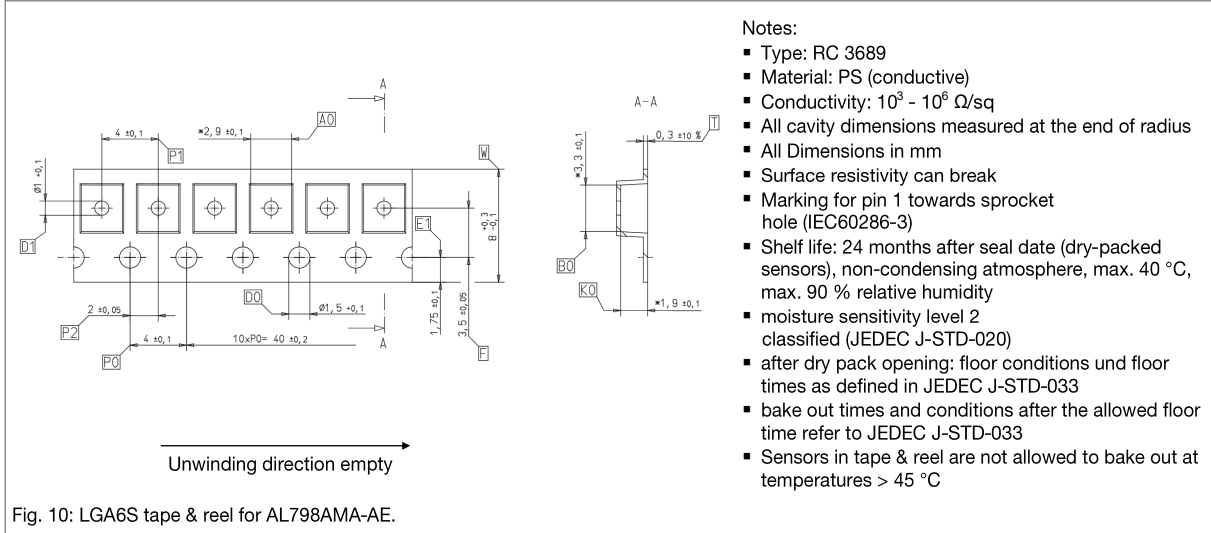


Fig. 10: LGA6S tape & reel for AL798AMA-AE.

#### Land pattern layout

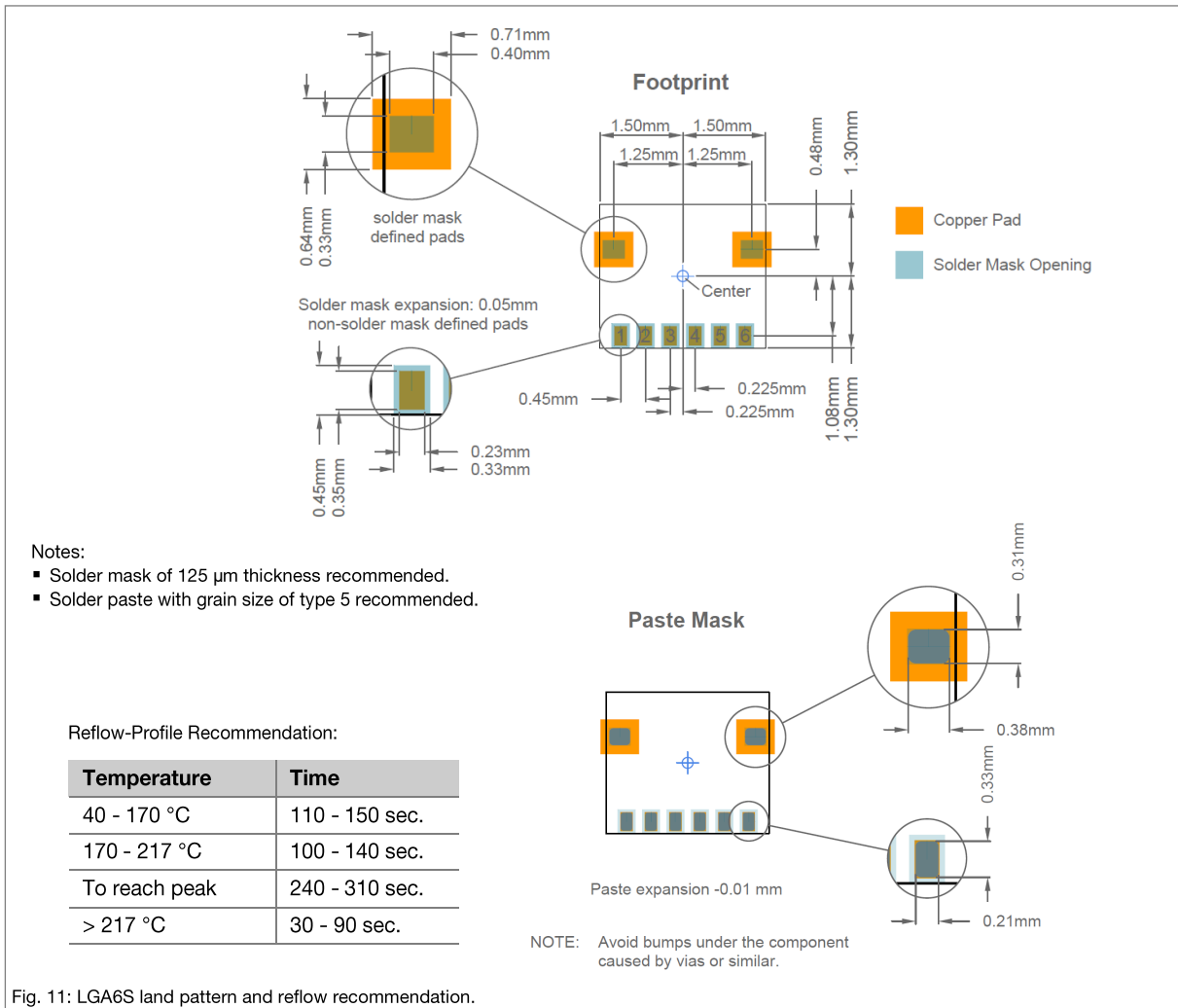


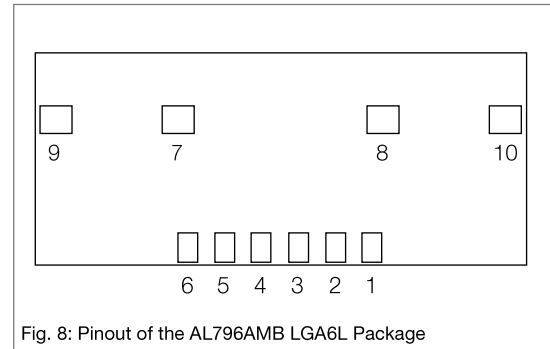
Fig. 11: LGA6S land pattern and reflow recommendation.



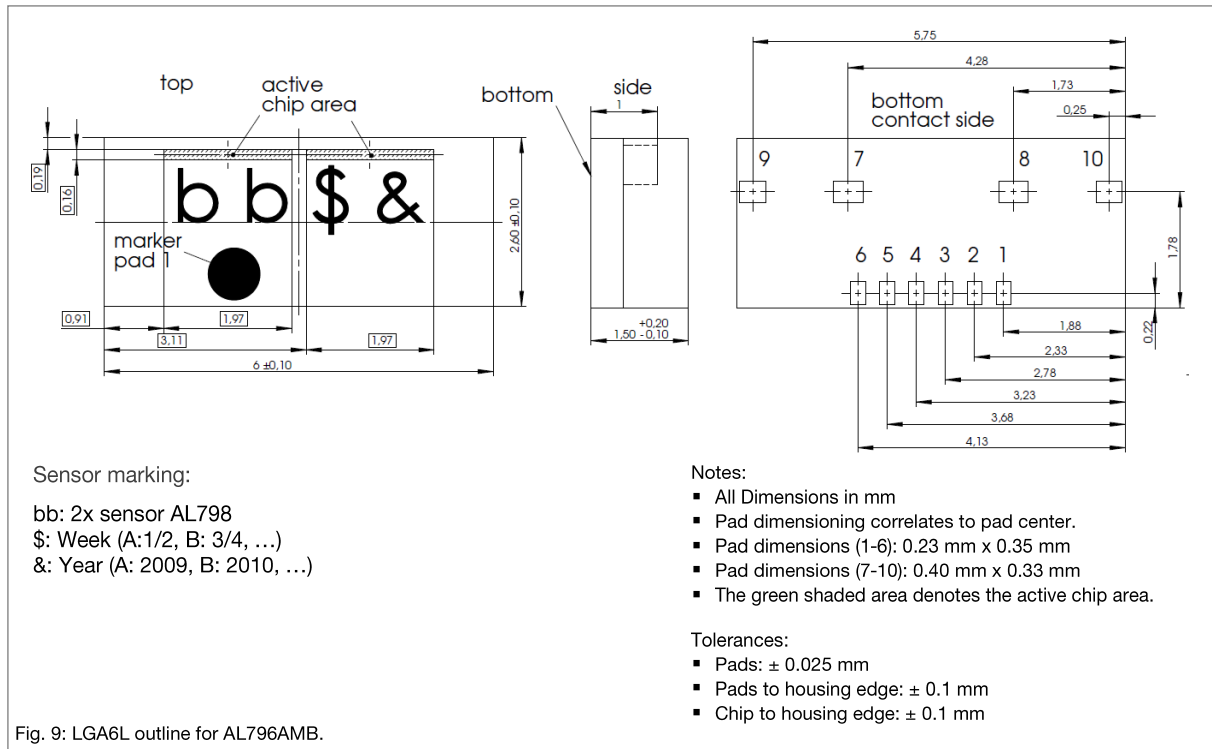
AL796AMB LGA6L Package

Pinout

Pad	Symbol	Parameter
1	+V <sub>O1</sub>	Positive output voltage bridge 1
2	+V <sub>O2</sub>	Positive output voltage bridge 2
3	GND	Ground
4	V <sub>CC</sub>	Supply voltage
5	-V <sub>O1</sub>	Negative output voltage bridge 1
6	-V <sub>O2</sub>	Negative output voltage bridge 2
7-10	NC	Not connected



Dimensions



**AL798AMB LGA6L Package**

**Reel layout**

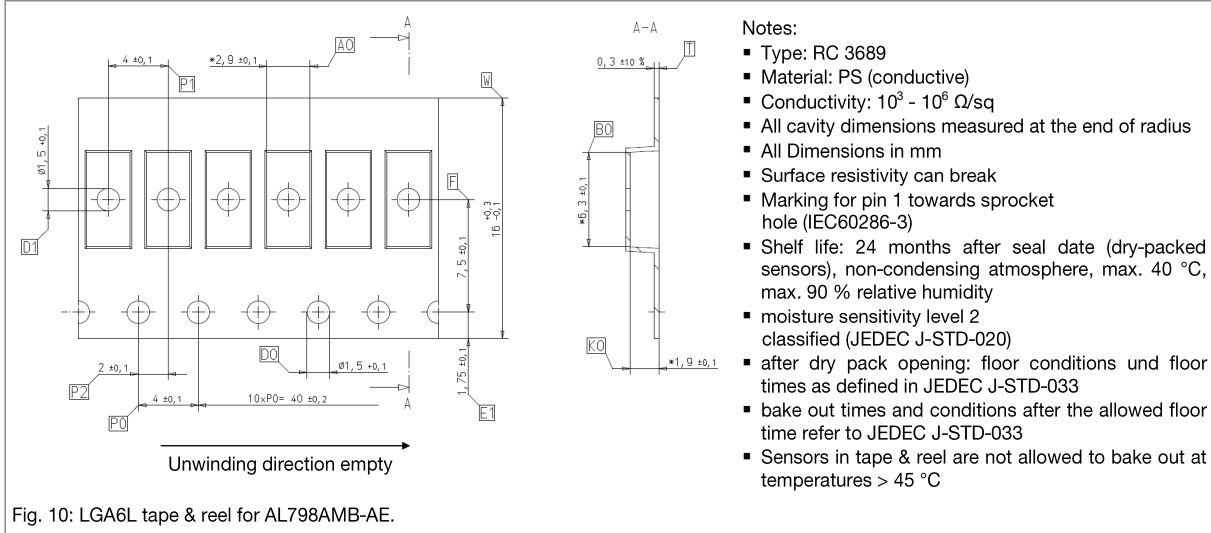


Fig. 10: LGA6L tape & reel for AL798AMB-AE.

**Land pattern layout**

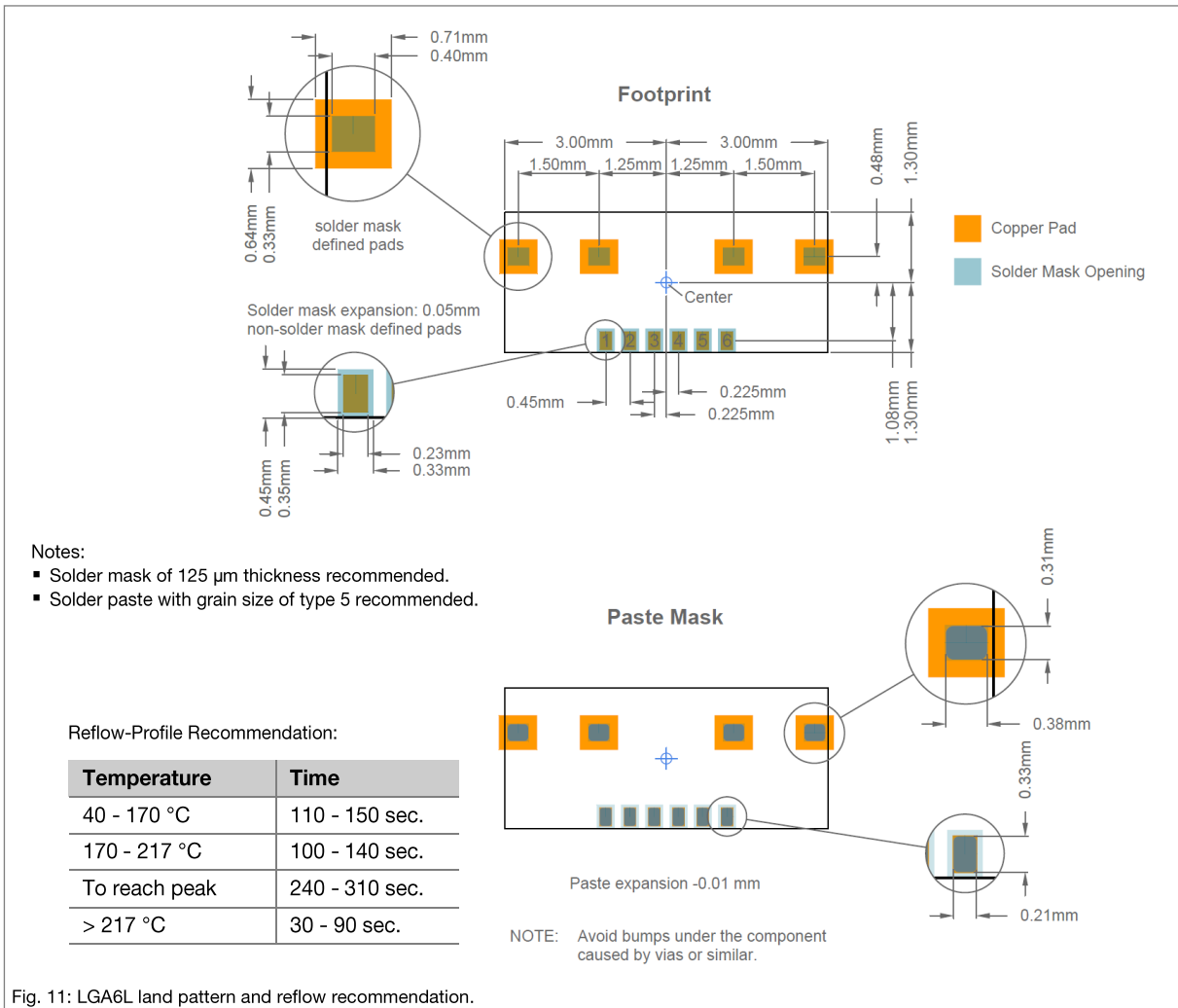


Fig. 11: LGA6L land pattern and reflow recommendation.

AL798AMS-AE SIL8 Package

Pinout

Pad	Symbol	Parameter
1	NC	Not connected
2	+V <sub>O1</sub>	Positive output voltage bridge 1
3	+V <sub>O2</sub>	Positive output voltage bridge 2
4	V <sub>CC</sub>	Supply voltage
5	GND	Ground
6	-V <sub>O1</sub>	Negative output voltage bridge 1
7	-V <sub>O2</sub>	Negative output voltage bridge 2
8	NC	Not connected

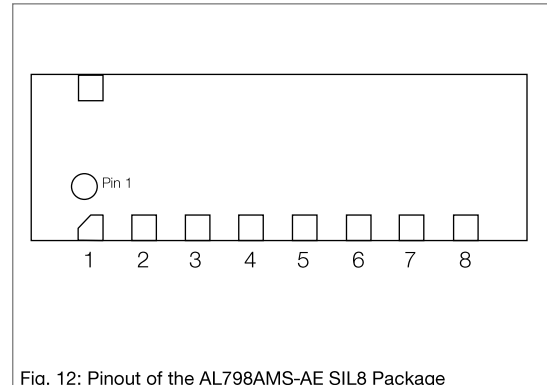


Fig. 12: Pinout of the AL798AMS-AE SIL8 Package

Dimensions

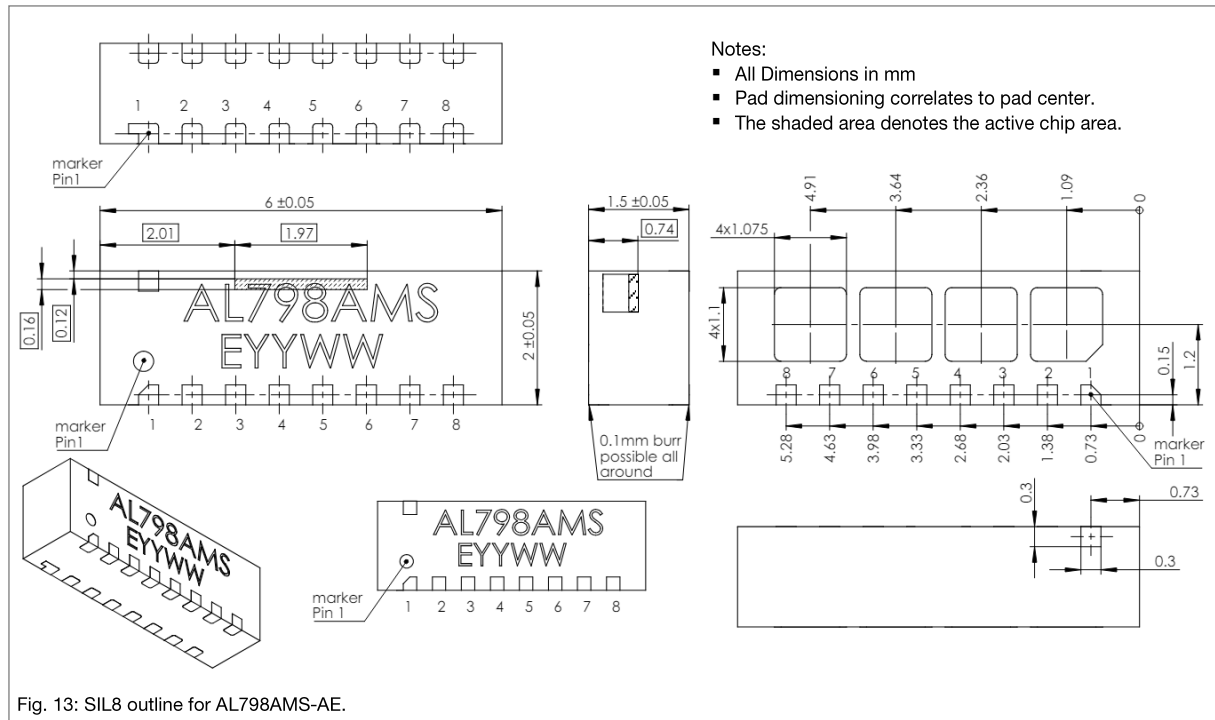


Fig. 13: SIL8 outline for AL798AMS-AE.

**AL798AMS-AE SIL8 Package**

**Reel layout**

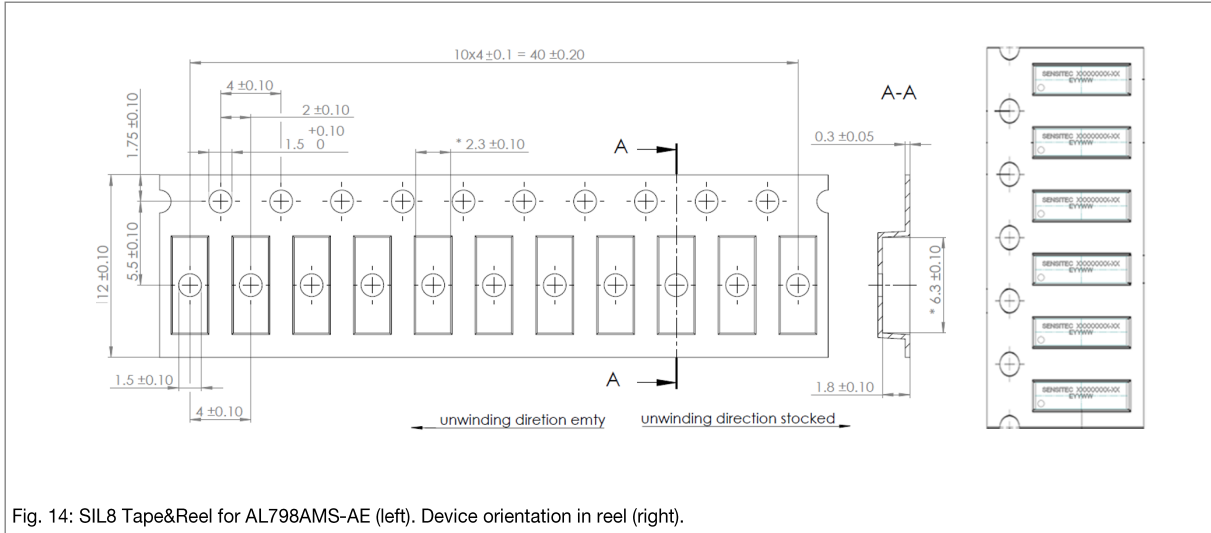


Fig. 14: SIL8 Tape&Reel for AL798AMS-AE (left). Device orientation in reel (right).

**Land pattern layout**

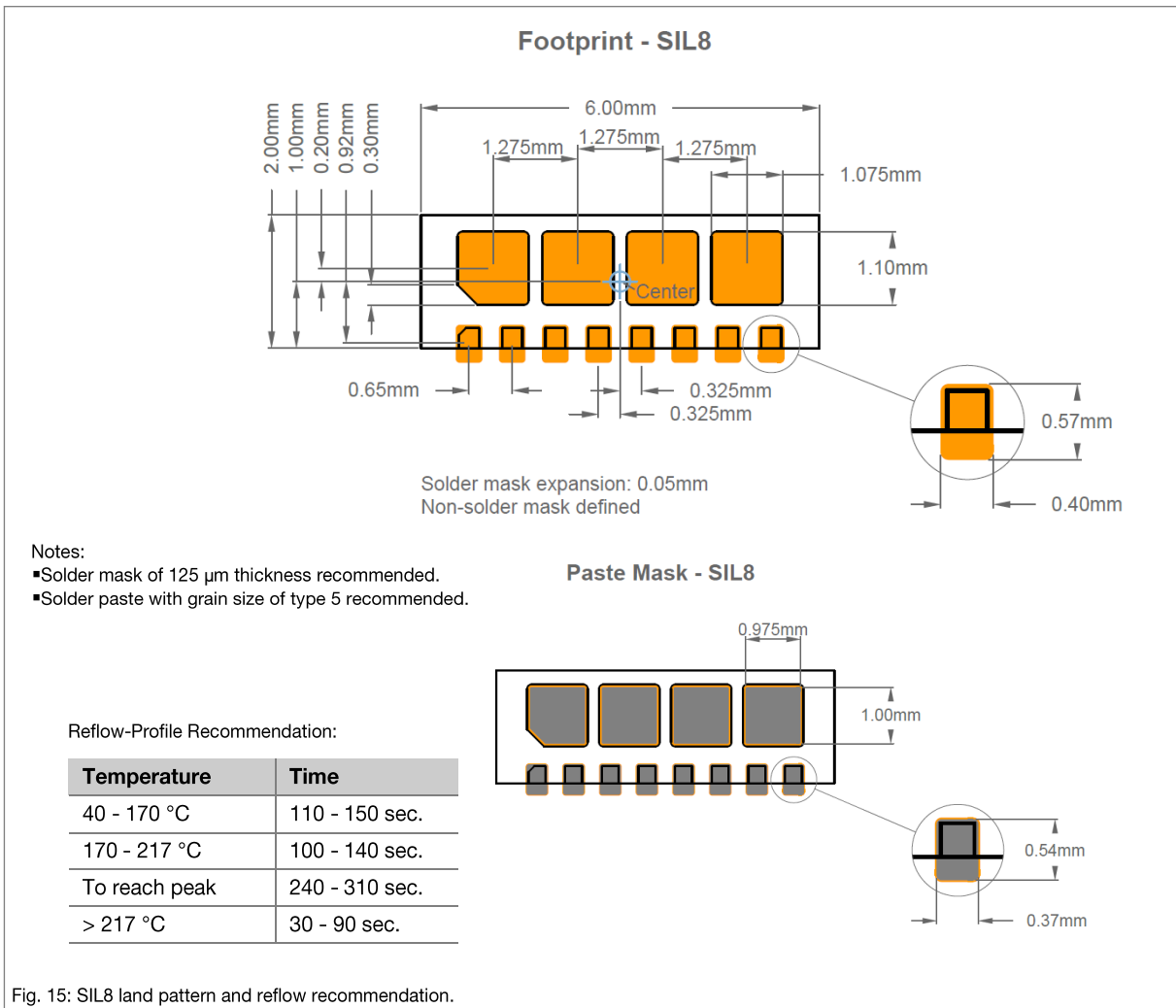
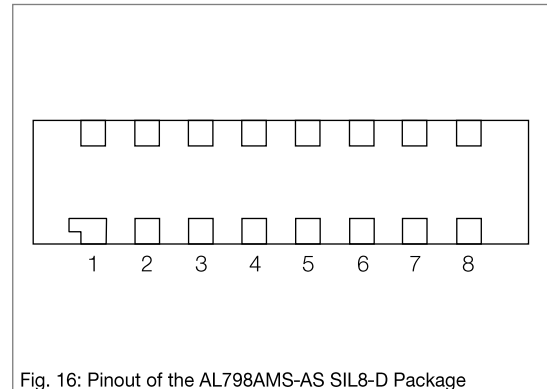


Fig. 15: SIL8 land pattern and reflow recommendation.

### AL798AMS-AS SIL8-D Package

#### Pinout

Pad	Symbol	Parameter
1	NC	Not connected
2	+V <sub>O1</sub>	Positive output voltage bridge 1
3	+V <sub>O2</sub>	Positive output voltage bridge 2
4	V <sub>CC</sub>	Supply voltage
5	GND	Ground
6	-V <sub>O1</sub>	Negative output voltage bridge 1
7	-V <sub>O2</sub>	Negative output voltage bridge 2
8	NC	Not connected



#### Dimensions

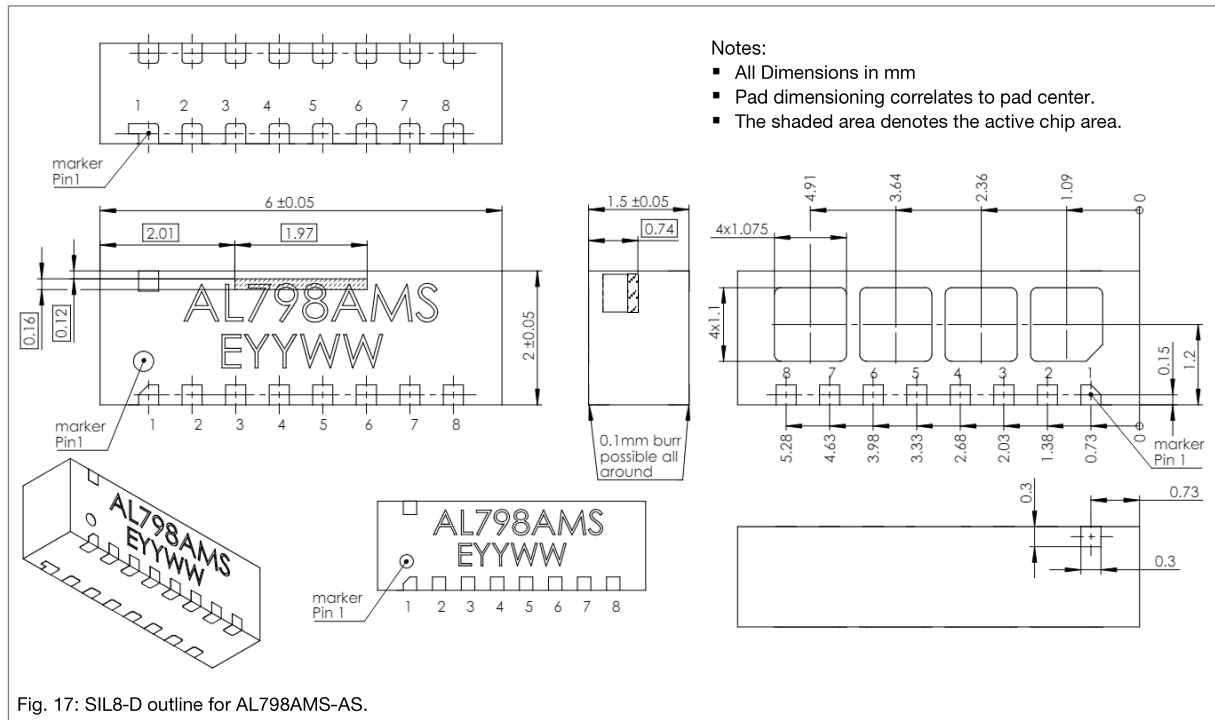


Fig. 17: SIL8-D outline for AL798AMS-AS.

### AL798AMS-AS SIL8-D Package

#### Reel layout

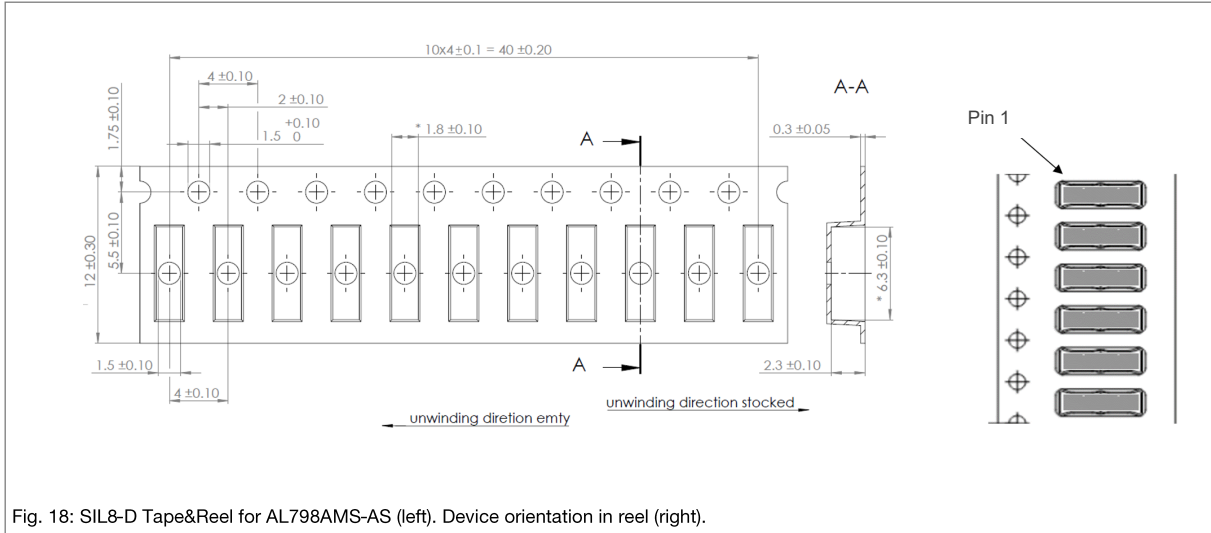


Fig. 18: SIL8-D Tape&Reel for AL798AMS-AS (left). Device orientation in reel (right).

#### Land pattern layout

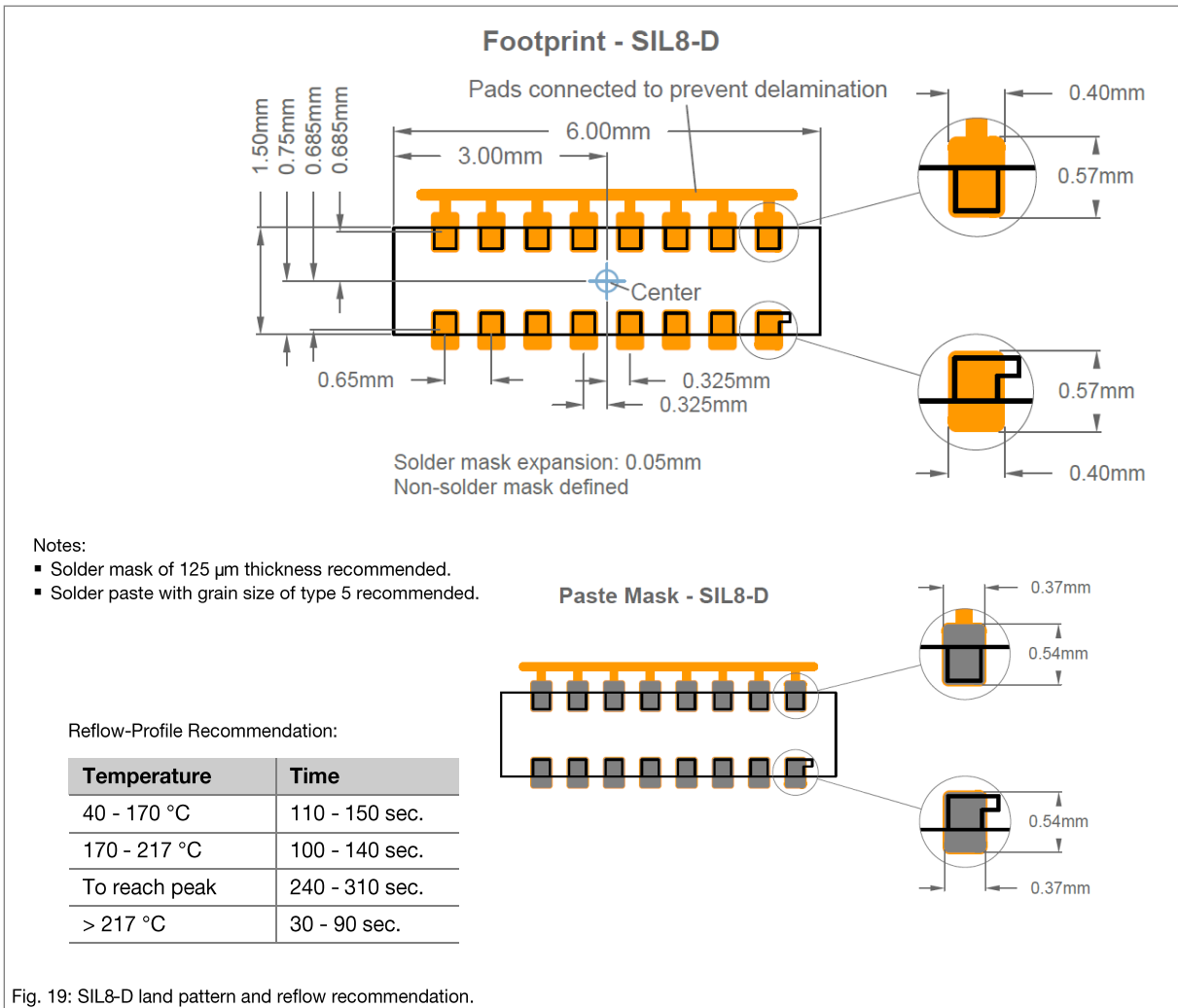
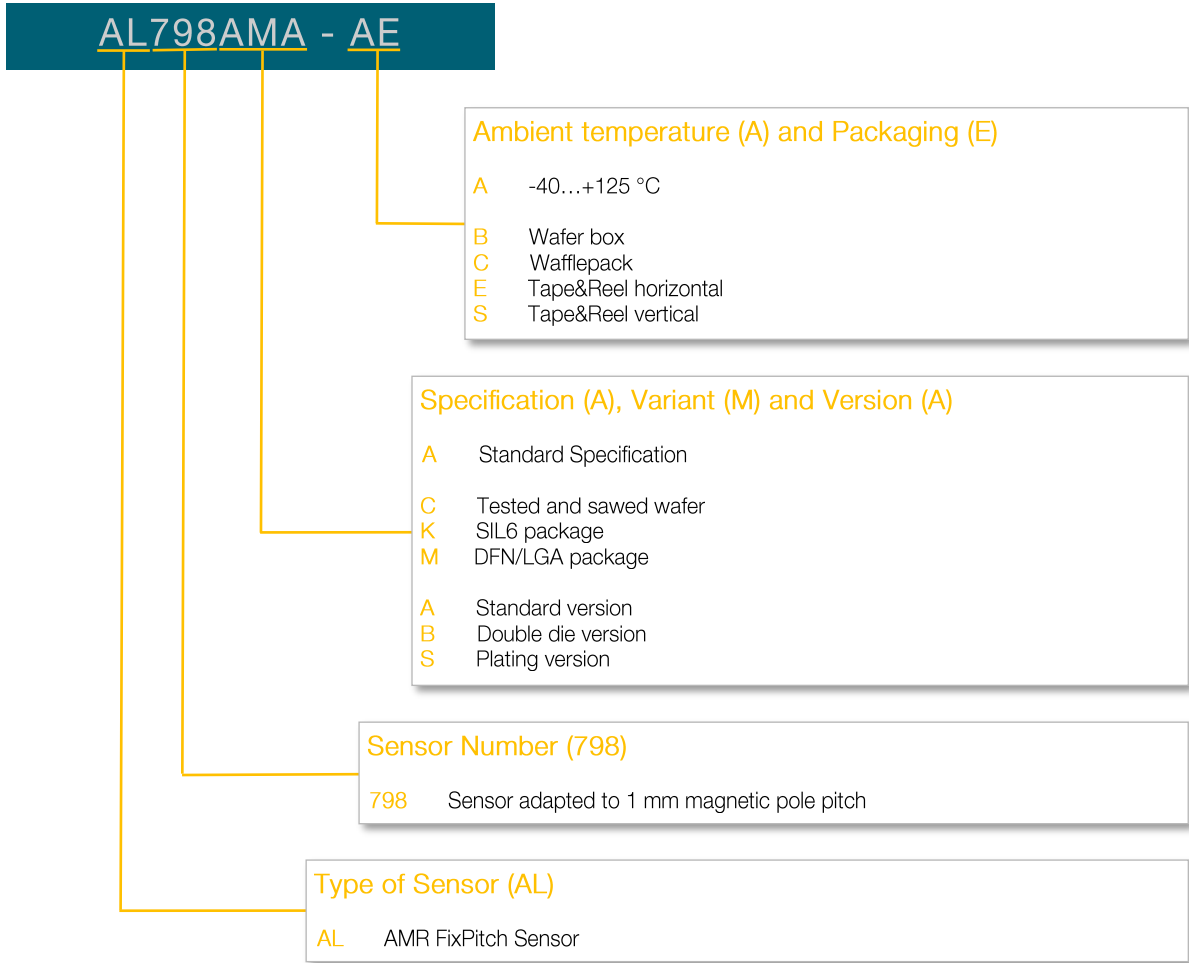


Fig. 19: SIL8-D land pattern and reflow recommendation.

**Additional Information on Ordering Code**

**Special Design Features**

**PerfectWave**

Sensors with PerfectWave design provide the best signal quality, highest accuracy and optimal sensor linearity by filtering out higher harmonics in the signal. The linearity of the sensor is assured, even for weak magnetic field measurement.


**PurePitch**

In PurePitch sensors, the FixPitch principle is extended over several poles in order to increase accuracy still further. This arrangement reduces the influence of errors in the measurement scale and improves the immunity to interference fields.


**FixPitch**

FixPitch sensors are adapted to the pole length (pitch) of the measurement scale. The linearity of the sensor is optimized and the influence of interference fields is minimized.

## General Information

### Product Status

Article	Status
AL798ACA-AB	The product is in series production.
AL798ACA-AC	The product is in series production.
AL798AKA-AC	The product is in series production.
AL798AMA-AE	The product is in series production.
AL798AMB-AE	The product is in series production.
AL798AMS-AE	The product is under development, qualification is on going. Deliverables have a sample status. The datasheet is preliminary.
AL798AMS-AS	The product is under development, qualification is on going. Deliverables have a sample status. The datasheet is preliminary.
<b>Note</b>	The status of the product may have changed since this data sheet was published. The latest information is available on the internet at <a href="http://www.sensitec.com">www.sensitec.com</a> .

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### Changelist

Version	Description of the Change	Date
AL798.DSE.14	Layout improvements (pp. 1-15), Change technical drawing (p. 11, p. 13), Add AL798AMB-AE (p. 9, p. 10)	05/2024
AL798.DSE.13	Disclaimer supplement	06/2022
AL798.DSE.12	Change of corporate design (pp. 1-10)	12/2021
AL798.DSE.00	Original (pp. 1-10)	10/2012

### Sensitec GmbH

Schanzenfeldstr. 2 • 35578 Wetzlar • Germany  
 Tel +49 (0) 6441 5291-0 • Fax +49 (0) 6441 5291-117  
 sensitec@sensitec.com • www.sensitec.com